



CURRICULUM VITAE OF Mariconti, Andrea Luigi Francesco

Born in Pavia (PV), 06/28/1989 resident in via Monferrato 5, 27100 Pavia (PV)

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Work Experiences

- 16/02/2015 - (nowadays) Apprenticeship at Università degli Studi di Pavia, via Ferrata 5, 27100 Pavia; work at the museum Ondivaghiamo. Main tasks: design, improvement or restoration of experiments of the museum; assistance to the professor during the lessons or the events at Ondivaghiamo.
- 9/2010 – 7/2013 Website designer / assistant to trades at Autofficina Anelli Carlo, viale Golgi 16, 27100 Pavia: co-operation in designing and maintaining the website www.anelliauto.it; interaction with managers of car showrooms and with private citizens interested in selling or buying cars.

Education

- 9/2014 – (nowadays) Student of Electronic Engineering (curriculum: Microelectronics) at Università degli Studi di Pavia, via Ferrata 5, 27100 Pavia.
- 9/2008 – 18/12/2014 Bachelor degree in Ingegneria Elettronica e delle Telecomunicazioni at Università degli Studi di Pavia, via Ferrata 5, 27100 Pavia.
- 2003 – 2008 Scientific high school diploma at Liceo Scientifico Statale Niccolò Copernico, via Verdi 23/25, 27100 Pavia.

Personal competences

- Mother tongue: Italian
- Other languages: English (FIRST certification, B2 level)
- Communication competences: Good communication competences, earned during my work at Autofficina Anelli as trades assistant.
- Organization capabilities: Good capability in organizing the team-work coordinating several persons for several tasks, earned during laboratory projects at university and during summer camps as an entertainer.
- Professional competences: Excellent ability in the use of the common electronic laboratory instruments; good ability in design and simulation of circuits and microcircuits using the principal software in the university laboratory (MPLab, Quartus 2, Cadence, OrCad Capture, OrCAD Layout, Microwave Office, PSpice).
- Computer competences: Excellent knowledge of Window's Office; excellent/good knowledge of some programming languages such as C++ (with a project with MPLab on an Easypic 5), VHDL (with a project with Quartus 2 on an FPGA), C, MIPS (with a project with SPIM); excellent/good knowledge of some projection or design softwares such as Cadence, OrCAD Capture, OrCAD Layout, PSpice, Microwave Office, HFSS, Matlab, Labview.
- Driving license: A1, B (motorbike and car owner)